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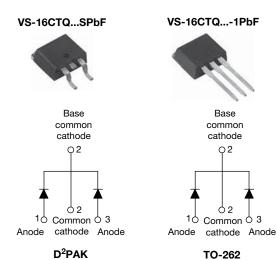
Vishay Semiconductors

RoHS

COMPLIANT

HALOGEN

High Performance Schottky Rectifier, 2 x 8 A



PRODUCT SUMMARY	
Package	TO-263AB (D ² PAK), TO-262AA
I _{F(AV)}	2 x 8 A
V _R	60 V to 100 V
V _F at I _F	0.58 V
I _{RM}	7 mA at 125 °C
T _J max.	175 °C
Diode variation	Common cathode
E _{AS}	7.5 mJ

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long **FREE** term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS A	OR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	16	A			
V _{RRM}		60 to 100	V			
I _{FSM}	t _p = 5 μs sine	850	А			
V _F	8 Apk, T _J = 125 °C (per leg)	0.58	V			
TJ	Range	-55 to 175	٥°			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-16CTQ060SPbF VS-16CTQ060-1PbF	VS-16CTQ080SPbF VS-16CTQ080-1PbF	VS-16CTQ100SPbF VS-16CTQ100-1PbF	UNITS
Maximum DC reverse voltage	V _R	60	80	100	V
Maximum working peak reverse voltage	V _{RWM}	00	00	100	V

ABSOLUTE MAXIMUM RATING	S				
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average per leg				8	
forward current per device	I _{F(AV)}	50 % duty cycle at T_C = 148 °C, rectangular waveform		16	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load	850	
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	275	A
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 0.50 \ A, \ L = 60$	mH	7.50	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	•	0.50	А

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ELECTRICAL SPECIFICATIONS
ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		8 A	T _{.1} = 25 °C	0.72	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	16 A	1j=25 C	0.88	v
	VFM (''	8 A	T.I = 125 °C	0.58	V
		16 A	1j = 125 C	0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B} = Rated V_{\rm B}$	0.55	mA
See fig. 2		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	7.0	
Threshold voltage	V _{F(TO)}			0.415	V
Forward slope resistance	r _t	$T_J = T_J maximum$		11.07	mΩ
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal ran	ge 100 kHz to 1 MHz), 25 °C	500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 n	nm from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANI	CAL SPE	CIFICAT	IONS			
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resistance, junction to case per leg		D		3.25		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.63	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	h and greased 0.50		
Approvimeto weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style D ² PAK	16CT	16CTQS	
Marking device			Case style TO-262	16CT	ຊ1	



VS-16CTQ...SPbF, VS-16CTQ...-1PbF Series

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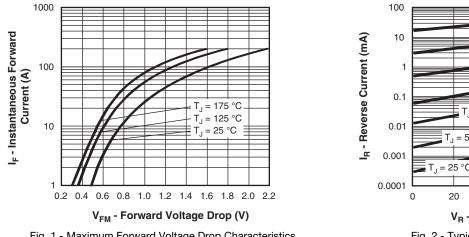
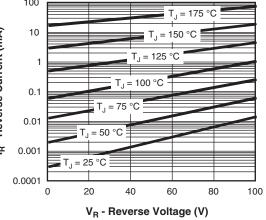
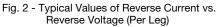


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





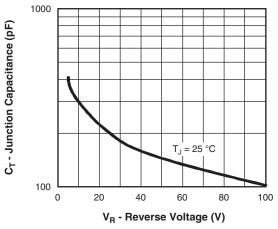


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

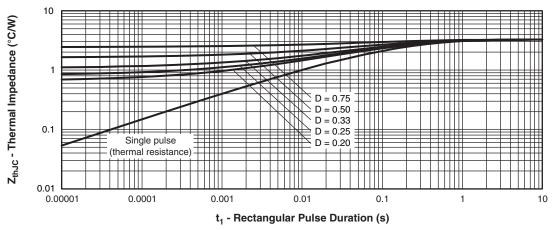
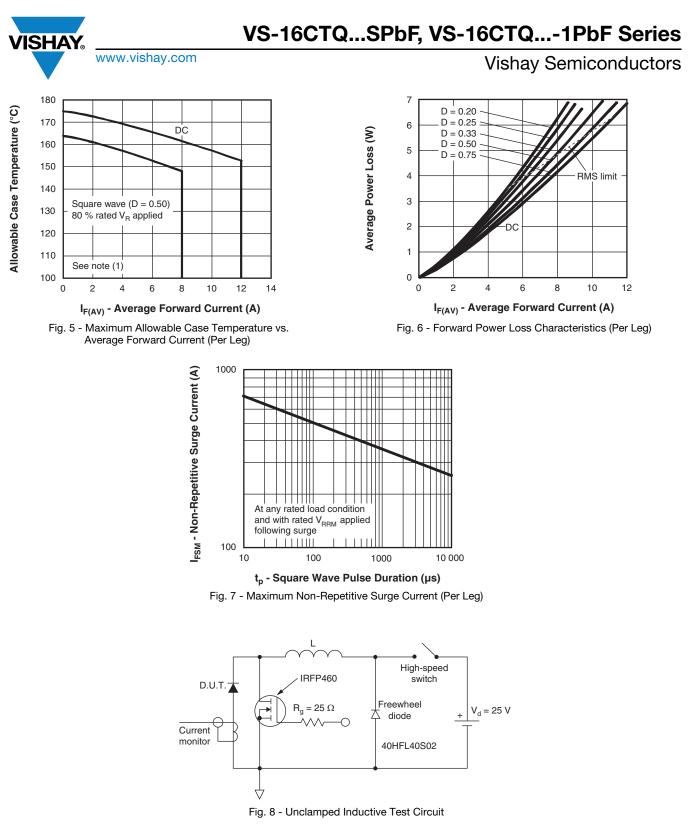


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 - Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 D)$; I_R at V_{R1} = 80 % rated V_R applied

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Vishay Semiconductors

ORDERING INFORMATION TABLE

VISHA

Device code	VS-	16	С	т	Q	100	S	TRL	PbF
		2	3	4	5	6	7	8	9
	 Vishay Semiconductors product suffix Current rating (16 A) Circuit configuration: C = Common cathode T = TO-220 								
	5 - 6 - 7 -	Volt • S	ottky "C age rati = D ² PA = TO-2	K	C)60 = 60)80 = 80)00 = 10	V		
l	8 -	• TI • TI	 None = Tube (50 pieces) TRL = Tape and reel (left oriented - for D²PAK only) TRR = Tape and reel (right oriented - for D²PAK only) PbF = Lead (Pb)-free 						

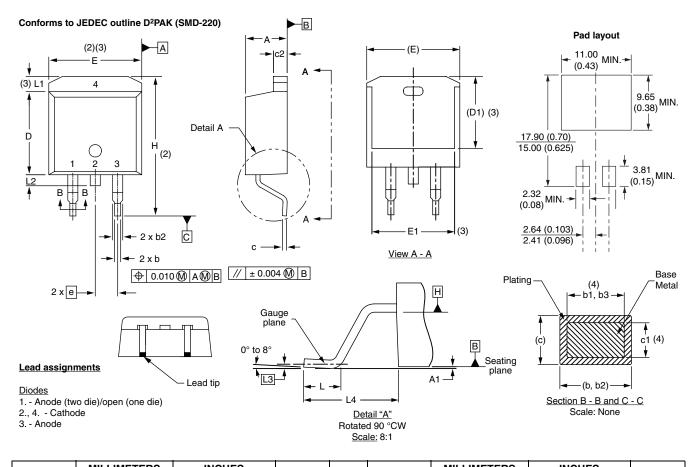
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95014		
Part marking information	www.vishay.com/doc?95008		
Packaging information	www.vishay.com/doc?95032		
SPICE model	www.vishay.com/doc?95279		

Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches

SHA



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

⁽⁷⁾ Outline conforms to JEDEC outline TO-263AB

Notes

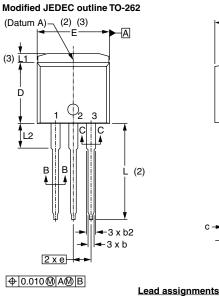
- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- ⁽⁴⁾ Dimension b1 and c1 apply to base metal only
- ⁽⁵⁾ Datum A and B to be determined at datum plane H
- ⁽⁶⁾ Controlling dimension: inch

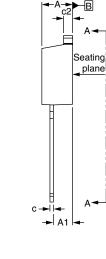
Vishay High Power Products

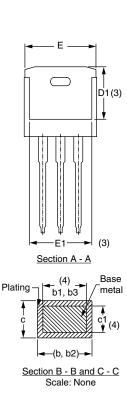
D²PAK, TO-262



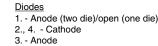
DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



	MILLIM	IETERS	INCH	IES	NOTEO
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- ⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

⁽⁶⁾ Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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