NPN Triple Diffused Planar Silicon Transistor



2SC3998

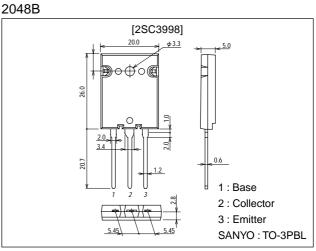
Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

Features

- \cdot High speed (t_f=100ns typ).
- \cdot High breakdown voltage (V_{CBO}=1500V).
- \cdot High reliability (adoption of HVP process).
- \cdot Adoption of MBIT process.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	ι _C		25	A
Collector Current (Pulse)	I _{CP}		50	A
Collector Dissipation	PC	Tc=25°C	250	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

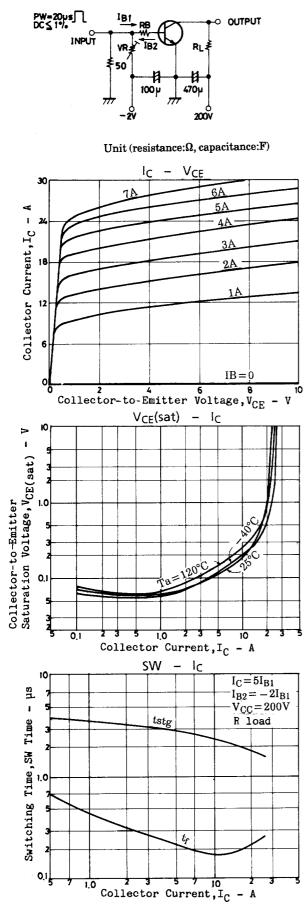
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ICES	V _{CE} =1500V			1.0	mA
Collector-to-Emitter Sastain Voltage	V _{CEO(sus)}	I _C =100mA, I _B =0	800			V
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0			1.0	mA
Collector Cutoff Current	I _{CBO}	V _{CB} =800V, I _E =0			10	μΑ
DC Current Gain	h _{FE} 1	V _{CE} =5V, I _C =1.0A	8		30	
	h _{FE} 2	V _{CE} =5V, I _C =20A	4		8	
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =20A, I _B =5A			5	V
Base-to-Emitter Saturation Voltage	VBE(sat)	I _C =20A, I _B =5A			1.5	V
Storage Time	^t stg	I _C =12A, I _{B1} =2.4A, I _{B2} =-4.8A			3.0	μs
Fall Time	t _f	I _C =12A, I _{B1} =2.4A, I _{B2} =-4.8A			0.2	μs

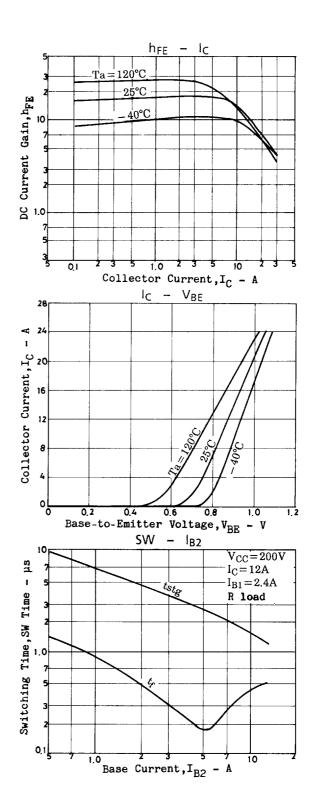
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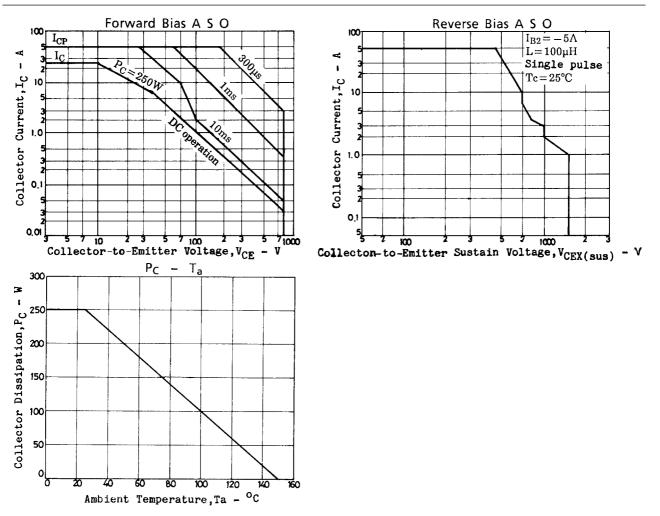
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Switching Time Test Circuit







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