NPN Triple Diffused Planar Silicon Transistor



# 2SC3998

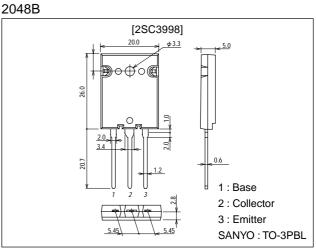
# Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

### Features

- $\cdot$  High speed (t<sub>f</sub>=100ns typ).
- $\cdot$  High breakdown voltage (V\_{CBO}=1500V).
- $\cdot$  High reliability (adoption of HVP process).
- $\cdot$  Adoption of MBIT process.

## **Package Dimensions**

unit:mm



## **Specifications**

### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	ι <sub>C</sub>		25	A
Collector Current (Pulse)	I <sub>CP</sub>		50	A
Collector Dissipation	PC	Tc=25°C	250	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics at Ta = 25°C**

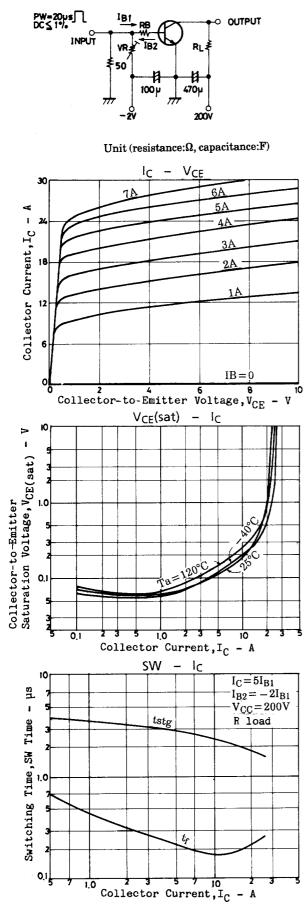
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ICES	V <sub>CE</sub> =1500V			1.0	mA
Collector-to-Emitter Sastain Voltage	V <sub>CEO(sus)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =0	800			V
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =4V, I <sub>C</sub> =0			1.0	mA
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =800V, I <sub>E</sub> =0			10	μΑ
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =5V, I <sub>C</sub> =1.0A	8		30	
	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =20A	4		8	
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =20A, I <sub>B</sub> =5A			5	V
Base-to-Emitter Saturation Voltage	VBE(sat)	I <sub>C</sub> =20A, I <sub>B</sub> =5A			1.5	V
Storage Time	<sup>t</sup> stg	I <sub>C</sub> =12A, I <sub>B1</sub> =2.4A, I <sub>B2</sub> =-4.8A			3.0	μs
Fall Time	t <sub>f</sub>	I <sub>C</sub> =12A, I <sub>B1</sub> =2.4A, I <sub>B2</sub> =-4.8A			0.2	μs

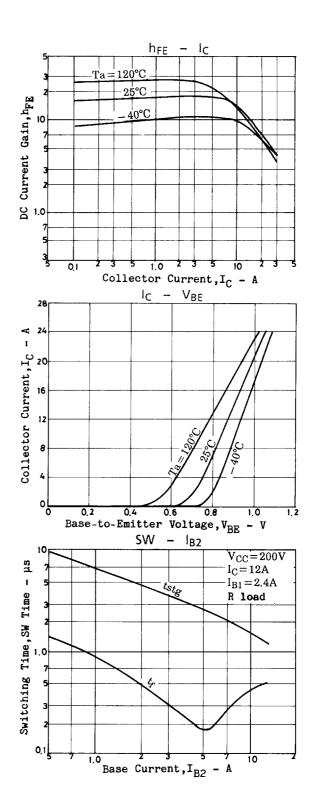
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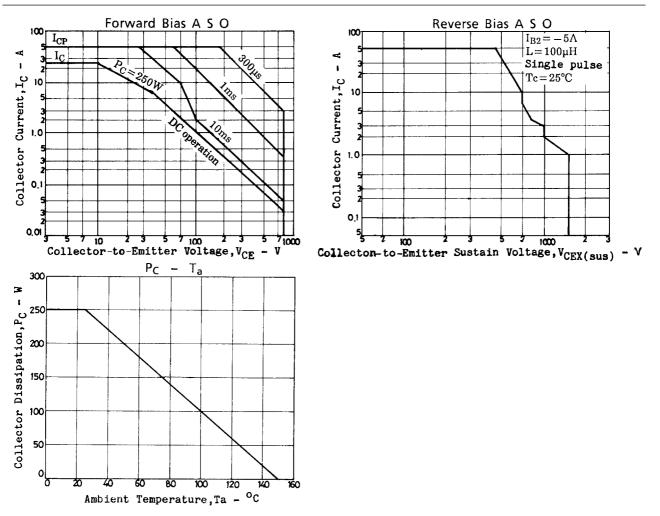
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### **Switching Time Test Circuit**







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