

### **Film Capacitors**

### EMI Suppression Capacitors (MKP)

Series/Type:B81123Date:June 2018

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### B81123

### **Typical applications**

- Y1 class for interference suppression
- "Line to ground" applications
- Double insulation

#### Climatic

- Max. operating temperature: 110 °C
- Climatic category (IEC 60068-1:2013): 40/110/56

### Construction

- Dielectric: polypropylene (MKP)
- Internal series connection
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

### Features

- Self-healing properties
- RoHS-compatible
- AEC-Q200D compliant

#### Terminals

- Parallel wire leads, lead-free tinned
- Standard lead lengths: 6 –1 mm
- Special lead lengths available on request

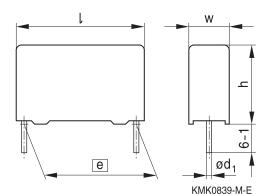
### Marking

Manufacturer's logo, lot number, date code, rated capacitance (coded), cap. tolerance (code letter), rated AC voltage, series number, sub-class (Y1), dielectric code (MKP), climatic category, passive flammability category, approvals.

#### **Delivery mode**

Bulk (untaped) Taped (Ammo pack or reel) For taping details, refer to chapter "Taping and packing".

### **Dimensional drawing**



Dimensions in mm

Lead spacing e ±0.4	Lead diameter d <sub>1</sub>
15, 22.5	0.8

### Marking example (position of marks may vary):



KMK1557-M



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### Y1

### Approvals

Approval marks	Standards	Certificate
<b>3</b> 10	EN 60384-14:2014 IEC 60384-14:2013	ENEC-01093
<b>3</b> 15	EN 60384-14:2014 IEC 60384-14:2013	138584
<b>91</b>	UL 1414:2000	E97863
c <b>911</b> us	UL 60384-14:2014 CSA E60384-14:2013	E97863 (approved by UL)

Notes:	Effective January 2014, only for EMI supression capacitors:
	<ul> <li>UL 60384-14:2014 certification replaces both UL 1414:2000 and UL 1283:2005 standards.</li> </ul>
	<ul> <li>CSA C22.2 No.1:2004 and CSA C22.2 No.8:2013 are replaced by CSA E60384-14:2013.</li> </ul>
	<ul> <li>References like 1414, 1283 are removed from the capacitor marking.</li> </ul>
	Capacitors under UL 1414:2000, UL 1283:2005 produced during or before 2013, are accepted under UL scope.
	Capacitors under CSA C22.2 No.1:2004 / CSA C22.2 No.8:2013 produced during or before 2013, are accepted under UL scope.

### Overview of available types

Lead spacing	15 mm	22.5 mm
C <sub>R</sub> (μF)		
0.0010		
0.0015		
0.0022		
0.0033		
0.0047		
0.0056		
0.0068		
0.010		



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### Ordering codes and packing units

Lead	C <sub>R</sub>	Max. dimensions	Ordering code	Ammo	Reel	Untaped
spacing		$w \times h \times I$	(composition see	pack		
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
15	0.0010	$5.0\times10.5\times18.0$	B81123C1102M***	4680	5200	4000
	0.0015	$6.0\times11.0\times18.0$	B81123C1152M***	3840	4400	4000
	0.0022	$7.0\times12.5\times18.0$	B81123C1222M***	3320	3600	4000
	0.0033	$8.5\times14.5\times18.0$	B81123C1332M***	2720	2800	2000
	0.0047	$9.0\times17.5\times18.0$	B81123C1472M***	2560	2800	2000
22.5	0.0056	$7.0\times16.0\times26.5$	B81123C1562M***	2320	2400	2520
	0.0068	$8.5\times16.5\times26.5$	B81123C1682M***	1920	2000	2040
	0.010	$10.5\times18.5\times26.5$	B81123C1103M***	1560	1600	2160

MOQ = Minimum Order Quantity, consisting of 4 packing units.

### Composition of ordering code

+ = Capacitance tolerance code:

M =±20%

- \*\*\* = Packaging code:
  - 289 = Straight terminals, Ammo pack
  - 189 = Straight terminals, Reel
  - $\begin{array}{rl} \text{003} = & \text{Straight terminals, untaped} \\ & (\text{lead length 3.2 } \pm 0.3 \text{ mm}) \end{array}$
  - 000 = Straight terminals, untaped (lead length 6 -1 mm)



B81123 Y1 / 500 V AC

## Y1

### **Technical data**

Reference standard: IEC 60384-14:2013 / UL 60384-14:2014. All data given at T = 20  $^{\circ}$ C, unless otherwise specified.

+110 °C			
at 1 kHz 1.0			
at 100 kHz 5.0			
30 000 MΩ			
4800 V, 2 s			
may damage the capacitor. Special care must be taken in			
rallel configuration.			
В			
750 V (50/60 Hz)			
500 V (50/60 Hz)			
3000 V			
$T_{op} \le 110 \ ^{\circ}C$ $V_{op} = V_{AC}$ (continuously)			
$T_{op} \le 110 \ ^{\circ}C$ $V_{op} = 1.25 \ \cdot \ V_{AC}$ (1000 h)			
56 days / 40 °C / 93% relative humidity			
Capacitance change $ \Delta C/C  \leq 5\%$			
Dissipation factor change $\Delta$ tan $\delta \leq 5.10^{-3}$ (at 1 kHz)			
≤ 1.0 · 10 <sup>-3</sup> (at 100 kHz)			
Insulation resistance $R_{ins} \ge 50\%$ of minimum			
or time constant $\tau = C_B \cdot R_{ins}$ as-delivered values			





### Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in  $V/\mu s$ .

" $k_0$ " represents the maximum permissible pulse characteristic of the waveform applied to the capacitor, expressed in V<sup>2</sup>/µs.

Note:

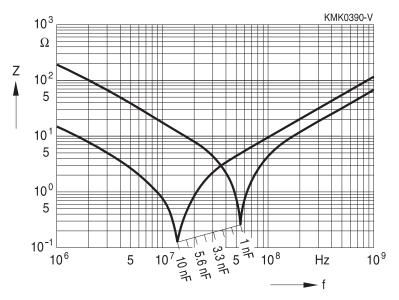
The values of dV/dt and  $k_0$  provided below must not be exceeded in order to avoid damaging the capacitor.

### dV/dt and k<sub>0</sub> values

Lead spacing	15 mm	22.5 mm
dV/dt in V/µs	3 000	1 000
k₀ in V²/μs	2 100 000	700 000

### Impedance Z versus frequency f

(typical values)





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# Y1

### **Testing and Standards**

Test	Reference	Conditions of test		Performance requirements
Electrical	IEC	Voltage Proof:		Within specified limits
parameters	60384-14:2013	Between terminals: 4000 V AC, 1 min		
		Terminals and enclo	osure:	
		4000 V AC, 1 min		
		Insulation resistance	e, R <sub>ins</sub>	
		Capacitance, C		
_		Dissipation factor, ta	an δ	
Robustness	IEC	Tensile strength (te	st Ua1)	Capacitance and tan $\delta$
of termina-	60068-2-21:2006	Wire diameter	Tensile	within specified limits
tions			force	
		$0.5 < d_1 \le 0.8 \text{ mm}$	10 N	
		$0.8 < d_1 \le 1.25 \text{ mm}$	20 N	
Resistance	IEC	Solder bath tempera	ature at	$\Delta C/C_0 \leq 5\%$
to soldering	60068-2-20:2008,	260 ±5 °C, immersi	on for	tan $\delta$ within specified limits
heat	test Tb,	10 seconds		
	method 1A			
Rapid	IEC	$T_A =$ lower category temperature		No visible damage
change of	60384-14:2013	$T_{B}$ = upper category temperature		$ \Delta C/C_0  \le 5\%$
temperature		Five cycles, duration t = 30 min.		tan $\delta$ within specified limits
Vibration	IEC	Test F <sub>c</sub> : vibration sinusoidal Displacement: 0.75 mm Accleration: 98 m/s <sup>2</sup> Frequency: 10 Hz 500 Hz		No visible damage
	60384-14:2013			
		Test duration: 3 orth	nogonal axes,	
		2 hours each axe		
Bump	IEC	Test Eb: Total 4000	0 bumps with	No visible damage
	60384-14:2013	400 m/s <sup>2</sup> mounted on PCB		$ \Delta C/C_0  \le 5\%$
		6 ms duration		tan $\delta$ within specified limits
Climatic	IEC	Dry heat Tb / 16 h		No visible damage
sequence	60384-14:2013	Damp heat cyclic, 1	<sup>st</sup> cycle	$ \Delta C/C_0  \le 5\%$
-		+55 °C / 24 h / 95%	100% RH	$ \Delta \tan \delta  \le 0.008$ for C $\le 1 \ \mu F$
		Cold Ta / 2 h		$ \Delta \tan \delta  \le 0.005$ for C > 1 $\mu$ F
		Damp heat cyclic, 5	cycles	Voltage proof
		+55 °C / 24 h / 95%	100% RH	$R_{ins} \ge 50\%$ of initial limit



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Test	Reference	Conditions of test	Performance requirements
Damp heat,	IEC	Test Ca	No visible damage
steady	60384-14:2013	40 °C / 93% RH / 56 days	$ \Delta C/C_0  \le 5\%$
state			$ \Delta \tan \delta  \le 0.008$ for C $\le 1 \ \mu F$
			$ \Delta \tan \delta  \le 0.005$ for C > 1 $\mu$ F
			Voltage proof
			$R_{ins} \ge 50\%$ of initial limit
Impulse	IEC	3 impulses	No visible damage
test	60384-14:2013	Tb / 1.7 V <sub>B</sub> / 1000 hours,	$ \Delta C/C_0  \leq 10\%$
Endurance		1000 V <sub>BMS</sub> for 0.1 s every hour	$ \Delta \tan \delta  \le 0.008$ for C $\le 1 \ \mu F$
			$ \Delta \tan \delta  \le 0.005$ for C > 1 $\mu$ F
			Voltage proof
			$R_{ins} \ge 50\%$ of initial limit
Passive	IEC	Flame applied for a period of	В
flammability	60384-14:2013	time depending on capacitor	
		volume	

### **Mounting guidelines**

### 1 Soldering

### 1.1 Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20, test Ta, method 1.

Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2, test Ba: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

Solder bath temperature	235 ±5 °C
Soldering time	2.0 ±0.5 s
Immersion depth	2.0 + 0/-0.5 mm from capacitor body or seating plane
Evaluation criteria:	
Visual inspection	Wetting of wire surface by new solder ≥90%, free-flowing solder





### 1.2 Resistance to soldering heat

Resistance to soldering heat is tested to IEC 60068-2-20, test Tb, method 1. Conditions:

Series	Solder bath temperature	Soldering time
MKT boxed (except $2.5 \times 6.5 \times 7.2$ mm) coated uncoated (lead spacing >10 mm)	260 ±5 °C	10 ±1 s
MFP		
MKP (lead spacing >7.5 mm)		
MKT boxed (case $2.5 \times 6.5 \times 7.2$ mm)		5±1 s
<pre>MKP (lead spacing ≤7.5 mm) MKT uncoated (lead spacing ≤10 mm) insulated (B32559)</pre>		<4 s recommended soldering profile for MKT uncoated (lead spacing $\leq$ 10 mm) and insulated (B32559)
300 KMK1242-V		
$^{\circ}C$ $^{\circ}C$ $^{250}$ $^{200}$ $^{150}$ $^{150}$ $^{100}$ $^{50}$ $^{0}$ $^{50}$ $^{0}$ $^{50}$ $^{100}$ $^{150}$ $^{200}$ s 25 $^{100}$ s 20 s 25 $^{100}$	50	
Immersion depth	2.0 +0/-0.5 mm from cap	pacitor body or seating plane
Shield	Heat-absorbing board, $(1.5 \pm 0.5)$ mm thick, between	
	capacitor body and liquid solder	
Evaluation criteria:		
Visual inspection	No visible damage	
$\Delta C/C_0$	2% for MKT/MKP/MFP 5% for EMI suppression of	capacitors
tan δ	As specified in sectional s	•

Please read *Cautions and warnings* and *Important notes* at the end of this document.





### 1.3 General notes on soldering

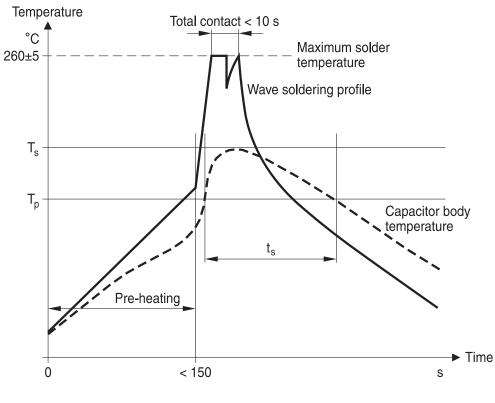
Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature  $T_{max}$ . Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics:
- diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

### **EPCOS recommendations**

As a reference, the recommended wave soldering profile for our film capacitors is as follows:

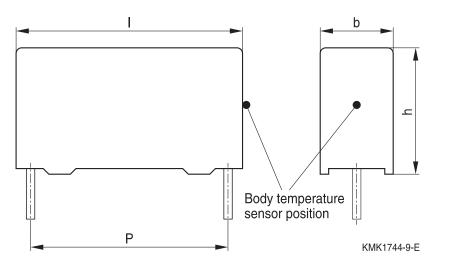


 $T_{s}: Capacitor body maximum temperature at wave soldering \\T_{p}: Capacitor body maximum temperature at pre-heating \\KMK1745-A-E$ 



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Body temperature should follow the description below:

- MKP capacitor During pre-heating: T<sub>p</sub> ≤110 °C During soldering: T<sub>s</sub> ≤120 °C, t<sub>s</sub> ≤45 s
- MKT capacitor During pre-heating: T<sub>p</sub> ≤125 °C During soldering: T<sub>s</sub> ≤160 °C, t<sub>s</sub> ≤45 s

When SMD components are used together with leaded ones, the film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.

Leaded film capacitors are not suitable for reflow soldering.

In order to ensure proper conditions for manual or selective soldering, the body temperature of the capacitor (T<sub>s</sub>) must be  $\leq$ 120 °C.

One recommended condition for manual soldering is that the tip of the soldering iron should be <360 °C and the soldering contact time should be no longer than 3 seconds.

For uncoated MKT capacitors with lead spacings  $\leq$ 10 mm (B32560/B32561) the following measures are recommended:

- pre-heating to not more than 110 °C in the preheater phase
- rapid cooling after soldering

Please refer to EPCOS Film Capacitor Data Book in case more details are needed.



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### **Cautions and warnings**

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.
- Consult us if application is with severe temperature and humidity condition.
- There are no serviceable or repairable parts inside the capacitor. Opening the capacitor or any attempts to open or repair the capacitor will void the warranty and liability of EPCOS.
- Please note that the standards referred to in this publication may have been revised in the meantime.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters "General technical information" and "Mounting guidelines".

Торіс	Safety information	Reference chapter
		"General technical
		information"
Storage	Make sure that capacitors are stored within the specified	4.5
conditions	range of time, temperature and humidity conditions.	"Storage conditions"
Flammability	Avoid external energy, such as fire or electricity (passive	5.3
	flammability), avoid overload of the capacitors (active	"Flammability"
	flammability) and consider the flammability of materials.	
Resistance to	Do not exceed the tested ability to withstand vibration.	5.2
vibration	The capacitors are tested to IEC 60068-2-6:2007.	"Resistance to
	EPCOS offers film capacitors specially designed for	vibration"
	operation under more severe vibration regimes such as	
	those found in automotive applications. Consult our	
	catalog "Film Capacitors for Automotive Electronics".	

Торіс	Safety information	Reference chapter
		"Mounting guidelines"
Soldering	Do not exceed the specified time or temperature limits	1 "Soldering"
	during soldering.	
Cleaning	Use only suitable solvents for cleaning capacitors.	2 "Cleaning"



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Торіс	Safety information	Reference chapter "Mounting guidelines"
Embedding of capacitors in finished assemblies	When embedding finished circuit assemblies in plastic resins, chemical and thermal influences must be taken into account. Caution: Consult us first, if you also wish to embed other uncoated component types!	3 "Embedding of capacitors in finished assemblies"

### Design of EMI Capacitors

EPCOS EMI capacitors use polypropylene (PP) film metalized with a thin layer of Zinc (Zn). The following key points have made this design suitable to IEC/UL testing, holding a minimum size.

- Overvoltage AC capability with very high temperature Endurance test of IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) must be performed at 1.25 × V<sub>R</sub> at maximum temperature, during 1000 hours, with a capacitance drift less than 10%.
- Higher breakdown voltage withstanding if compared to other film metallizations, like Aluminum. IEC 60384-14:2013 (4<sup>th</sup> edition) / UL 60384-14:2014 (2<sup>nd</sup> edition) establishes high voltage tests performed at 4.3 × V<sub>R</sub> −1 minute, impulse testing at 2500 V for C = 1 µF and active flammability tests.
- Damp heat steady state: 40 °C/ 93% RH / 56 days. (without voltage or current load)

### Effect of humidity on capacitance stability

Long contact of a film capacitor with humidity can produce irreversible effects. Direct contact with liquid water or excess exposure to high ambient humidity or dew will eventually remove the film metallization and thus destroy the capacitor. Plastic boxed capacitors must be properly tested in the final application at the worst expected conditions of temperature and humidity in order to check if any parameter drift may provoke a circuit malfunction.

In case of penetration of humidity through the film, the layer of Zinc can be degraded, specially under AC operation (change of polarity), accelerated by the temperature, provoking an increment of the serial resistance of the electrode and eventually a reduction of the capacitance value. For DC operation, the parameter drift is much less.

Plastic boxes and resins can not protect 100% against humidity. Metal enclosures, resin potting or coatings or similar measures by customers in their applications will offer additional protection against humidity penetration.

### Display of ordering codes for EPCOS products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of EPCOS, or in order-related documents such as shipping notes, order confirmations and product labels. **The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products**. Detailed information can be found on the Internet under <u>www.epcos.com/orderingcodes</u>.



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### Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
$\alpha_{c}$	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
А	Capacitor surface area	Kondensatoroberfläche
βc	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
С	Capacitance	Kapazität
C <sub>R</sub>	Rated capacitance	Nennkapazität
$\Delta C$	Absolute capacitance change	Absolute Kapazitätsänderung
$\Delta C/C$	Relative capacitance change (relative	Relative Kapazitätsänderung (relative
	deviation of actual value)	Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation	Kapazitätstoleranz (relative Abweichung
	from rated capacitance)	vom Nennwert)
dt	Time differential	Differentielle Zeit
$\Delta t$	Time interval	Zeitintervall
$\Delta T$	Absolute temperature change	Absolute Temperaturänderung
	(self-heating)	(Selbsterwärmung)
$\Delta tan \delta$	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
$\Delta V$	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate	Differentielle Spannungsänderung
	of voltage rise)	(Spannungsflankensteilheit)
$\Delta V / \Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f <sub>1</sub>	Frequency limit for reducing permissible	Grenzfrequenz für thermisch bedingte
	AC voltage due to thermal limits	Reduzierung der zulässigen
		Wechselspannung
f <sub>2</sub>	Frequency limit for reducing permissible	Grenzfrequenz für strombedingte
	AC voltage due to current limit	Reduzierung der zulässigen
		Wechselspannung
f <sub>r</sub>	Resonant frequency	Resonanzfrequenz
F <sub>D</sub>	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
$F_{T}$	Derating factor	Deratingfaktor
i	Current (peak)	Stromspitze
I <sub>C</sub>	Category current (max. continuous	Kategoriestrom (max. Dauerstrom)
	current)	



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Symbol	English	German
I <sub>RMS</sub>	(Sinusoidal) alternating current,	(Sinusförmiger) Wechselstrom
	root-mean-square value	
i <sub>z</sub>	Capacitance drift	Inkonstanz der Kapazität
k <sub>o</sub>	Pulse characteristic	Impulskennwert
Ls	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate
λο	Constant failure rate during useful	Konstante Ausfallrate in der
	service life	Nutzungsphase
$\lambda_{\text{test}}$	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
$P_{diss}$	Dissipated power	Abgegebene Verlustleistung
P <sub>gen</sub>	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des
		Entladekreises
R <sub>i</sub>	Internal resistance	Innenwiderstand
R <sub>ins</sub>	Insulation resistance	Isolationswiderstand
R <sub>P</sub>	Parallel resistance	Parallelwiderstand
Rs	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtetest)
t	Time	Zeit
Т	Temperature	Temperatur
τ	Time constant	Zeitkonstante
tan δ	Dissipation factor	Verlustfaktor
$\tan \delta_{D}$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
tan δ <sub>P</sub>	Parallel component of dissipation factor	Parallelanteil des Verlfustfaktors
tan $\delta_s$	Series component of dissipation factor	Serienanteil des Verlustfaktors
T <sub>A</sub>	Temperature of the air surrounding the component	Temperatur der Luft, die das Bauteil umgibt
T <sub>max</sub>	Upper category temperature	Obere Kategorietemperatur
T <sub>min</sub>	Lower category temperature	Untere Kategorietemperatur
t <sub>OL</sub>	Operating life at operating temperature	Betriebszeit bei Betriebstemperatur und
-	and voltage	-spannung
T <sub>op</sub>	Operating temperature, $T_A + \Delta T$	Beriebstemperatur, $T_A + \Delta T$
T <sub>R</sub>	Rated temperature	Nenntemperatur
T <sub>ref</sub>	Reference temperature	Referenztemperatur
t <sub>SL</sub>	Reference service life	Referenz-Lebensdauer



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Symbol	English	German
V <sub>AC</sub>	AC voltage	Wechselspannung
V <sub>c</sub>	Category voltage	Kategoriespannung
$V_{C,RMS}$	Category AC voltage	(Sinusförmige)
		Kategorie-Wechselspannung
$V_{CD}$	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
$V_{ch}$	Charging voltage	Ladespannung
$V_{\text{DC}}$	DC voltage	Gleichspannung
$V_{\text{FB}}$	Fly-back capacitor voltage	Spannung (Flyback)
Vi	Input voltage	Eingangsspannung
Vo	Output voltage	Ausgangssspannung
V <sub>op</sub>	Operating voltage	Betriebsspannung
V <sub>p</sub>	Peak pulse voltage	Impuls-Spitzenspannung
$V_{pp}$	Peak-to-peak voltage Impedance	Spannungshub
V <sub>R</sub>	Rated voltage	Nennspannung
ν <sub>R</sub>	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
$V_{\text{RMS}}$	(Sinusoidal) alternating voltage,	(Sinusförmige) Wechselspannung
	root-mean-square value	
$V_{SC}$	S-correction voltage	Spannung bei Anwendung "S-correction"
$V_{sn}$	Snubber capacitor voltage	Spannung bei Anwendung
		"Beschaltung"
Z	Impedance	Scheinwiderstand
е	Lead spacing	Rastermaß



The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
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#### Important notes

- 7. Our manufacturing sites serving the automotive business apply the IATF 16949 standard. The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that only requirements mutually agreed upon can and will be implemented in our Quality Management System. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.
- 8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.epcos.com/trademarks.

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